



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): KIMOTO, Hisamitsu

Serial No.: 09/850,254

Filed: May 7, 2001

Title: SEMICONDUCTOR APPARATUS
INCLUDING A MULTI-LAYER WIRING
CONFIGURATION AND
MANUFACTURING METHOD THEREFOR

Attorney Docket No.: N13476100S

Group Art Unit: 2826

Examiner: Quinto, K.

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Ext. 1 mo.
J. M. Chinnell
8/8/03

AMENDMENT AFTER FINAL ACTION
(37 C.F.R. §1.116)

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Commissioner for Patents
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A. Introductory Comments

The following is submitted in response to the FINAL Office Action dated March 26, 2003, and is currently due July 26, 2003, with a one month extension.

This response is presented in the revised amendment format set forth in 1265 Off. Gaz. Pat. Office 87 (Dec. 17, 2002) and so is subject to waiver of the provisions of 37 C.F.R. §1.21(a), (b) and (c).

37 C.F.R. §1.8

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